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of

1

Application Number	Not yet assigned
Filing Date	20 October 2003
First Named Inventor	Mark Beaumont
Art Unit	Not yet assigned
Examiner Name	Not yet assigned
Attorney Docket Number	D8001076-000

[illegible]

/Tan Mai/

Date Considered

11/13/2006

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PTO/SB/08A (08-03)

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Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet

1

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Complete if Known

Application Number	10/689,335
Filing Date	20 October 2003
First Named Inventor	Mark Beaumont
Art Unit	Not yet assigned
Examiner Name	Not yet assigned
Attorney Docket Number	DB001076-000

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)	MM-DD-YYYY			
TM		WO 2001/067238 A1	11/13/2001	Sun Microsystem		
V		EP 0404012 A3	12/27/1990	NEC Corp.		
		WO 1991/019259 A1	12/12/1991	Adaptive Solut.		

Examiner Signature	/Tan Mai/	Date Considered	11/13/2006
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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	Not Yet Assigned
				Filing Date	20 October 2003
				First Named Inventor	Mark Beaumont
				Art Unit	Not Yet Assigned
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	DB001076-000

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
TM		Daehyun Kim, Mainak Chaudhuri, and Mark Heinrich, Leveraging Cache Coherence in Active Memory Systems, Proceedings of the 16th ACM	
		Int'l Conference on Supercomputing, pages 2-13, New York City, USA, June 2002.	
		Mainak Chaudhuri, Daehyun Kim, and Mark Heinrich, Cache Coherence Protocol Design for Active Memory Systems, Proceedings of the 2002	
		Int'l Conference on Parallel and Distributed Processing Techniques and Applications, pages 83-89, Las Vegas, USA, June 2002.	

Examiner Signature	/Tan Mai/	Date Considered	11/13/2006
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